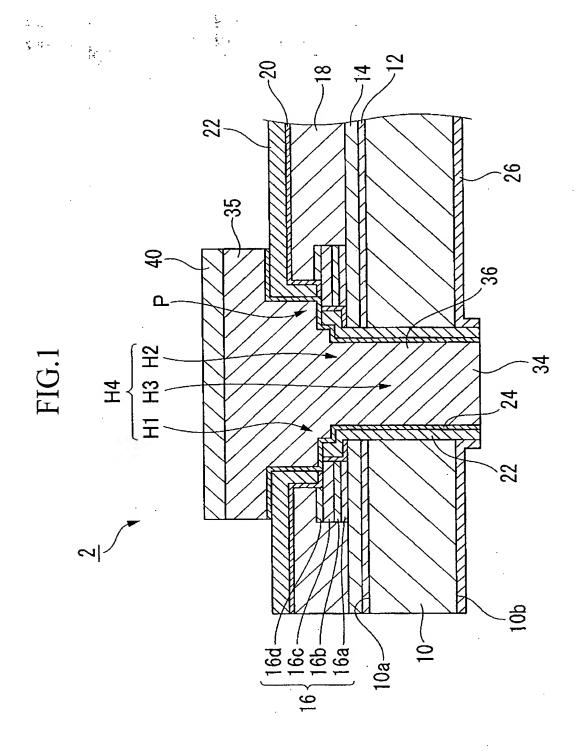
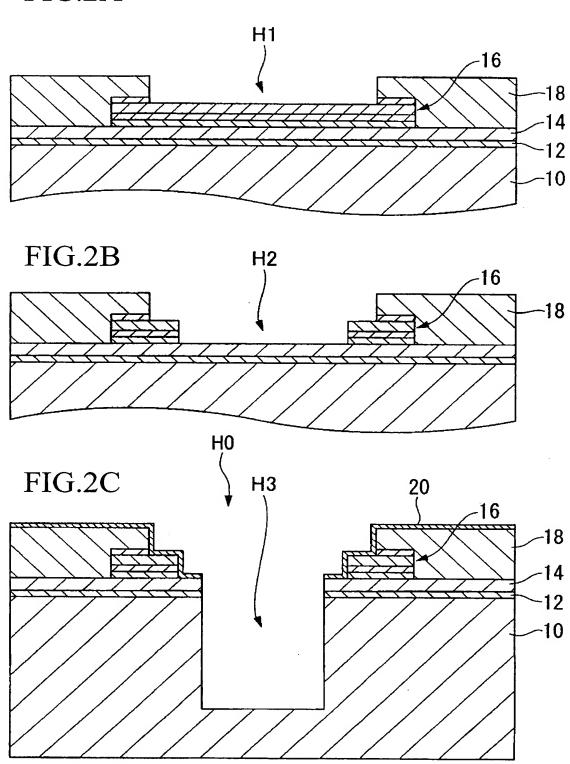
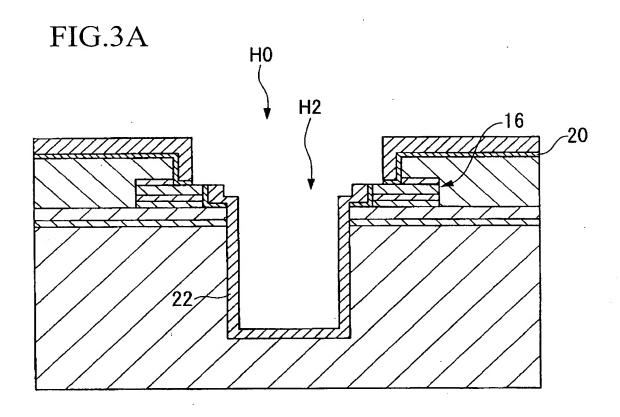
641 -

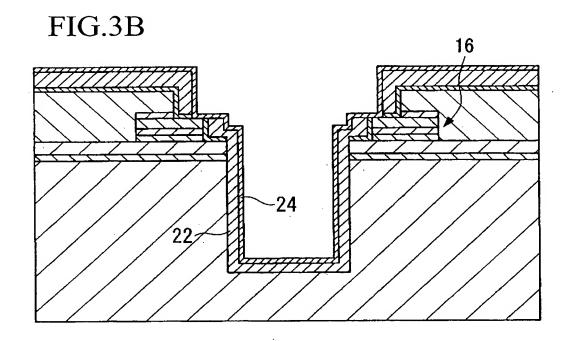


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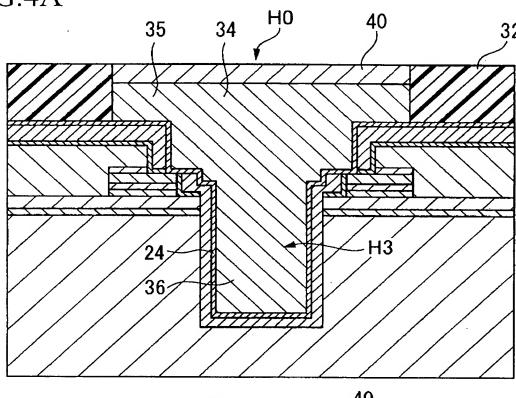
FIG.2A











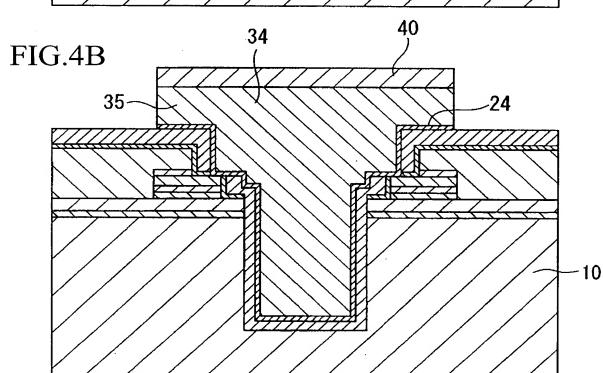


FIG.5A

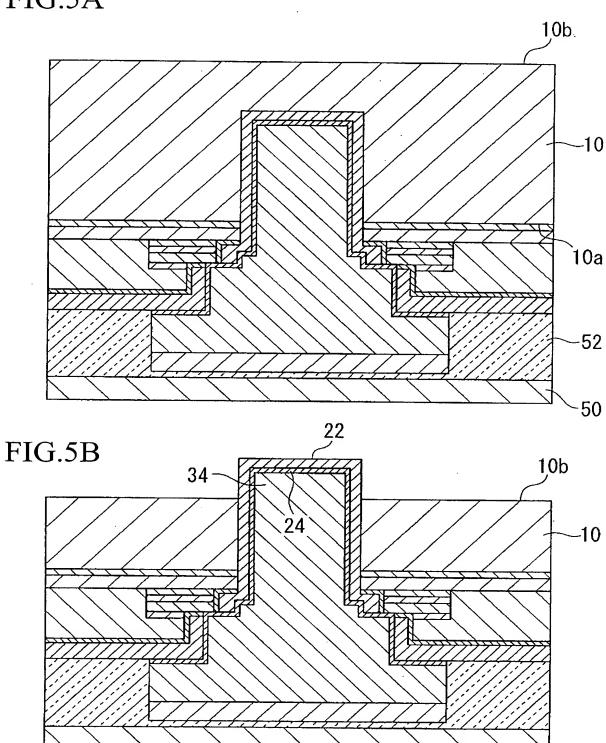
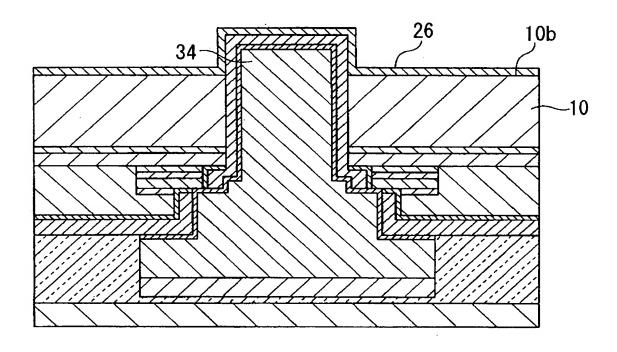


FIG.6A



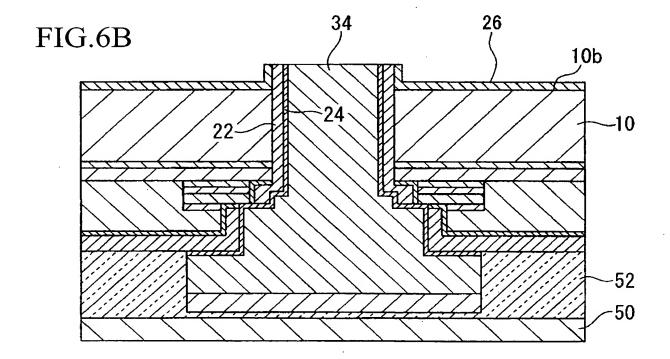
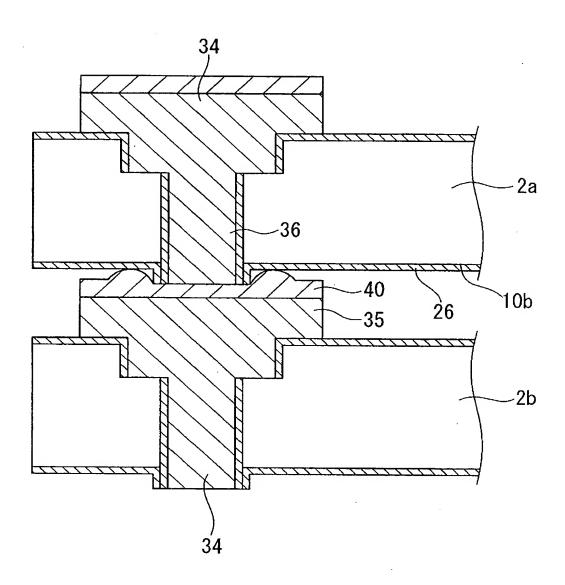


FIG.7



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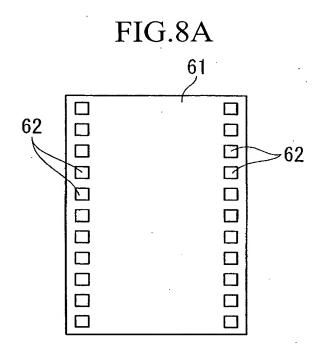
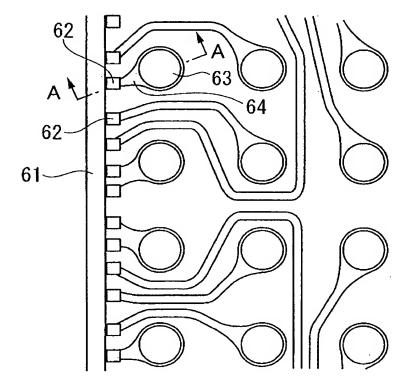


FIG.8B



Title: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE, SEMICONDUCTOR DEVICE, CIRCUIT SUBSTRATE AND ELECTRONIC APPARATUS Inventor(s): Ikuya MIYAZAWA Atty. Ref.: 9319G-000739

FIG.9

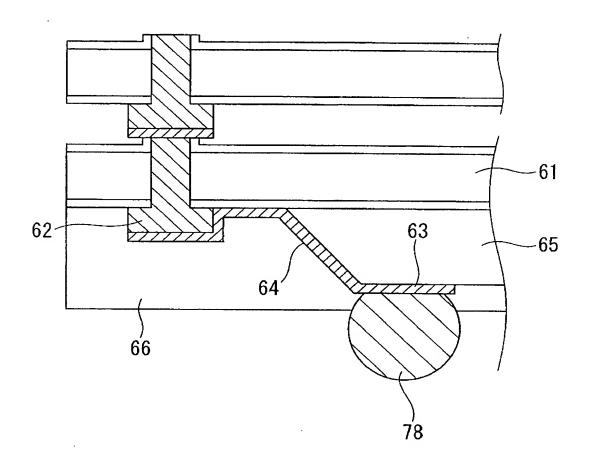
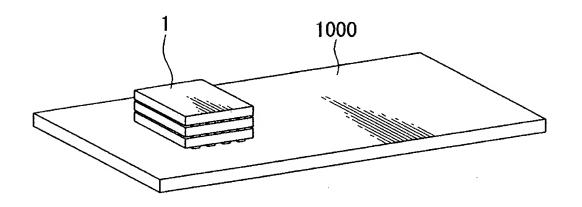


FIG.10

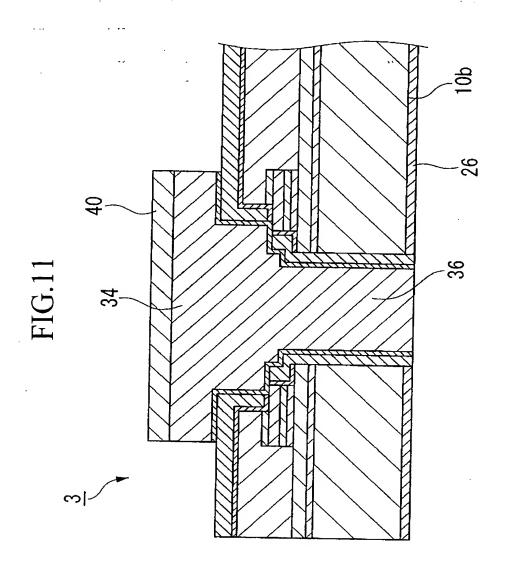


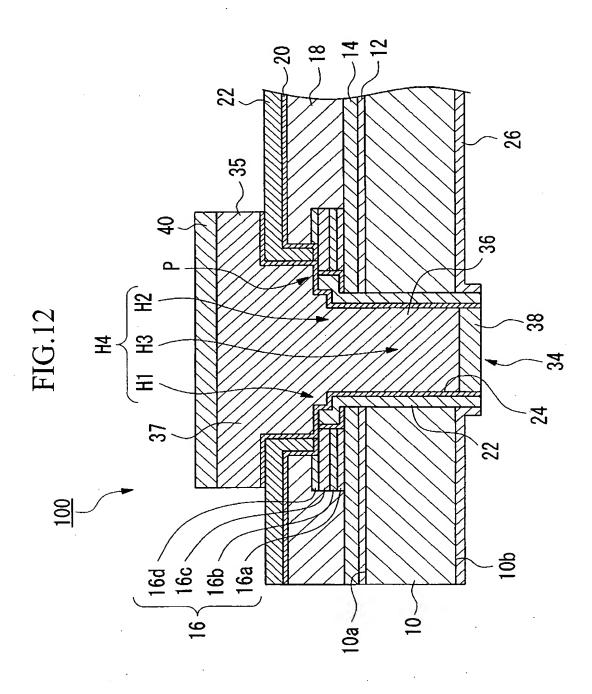
Title: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE, SEMICONDUCTOR DEVICE, CIRCUIT SUBSTRATE AND ELECTRONIC APPARATUS Inventor(s): Ikuya MIYAZAWA
Atty. Ref.: 9319G-000739

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ilr.

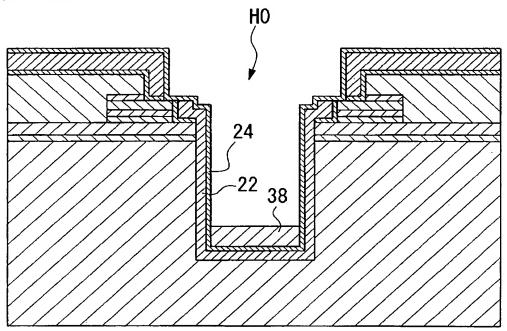
. 25-

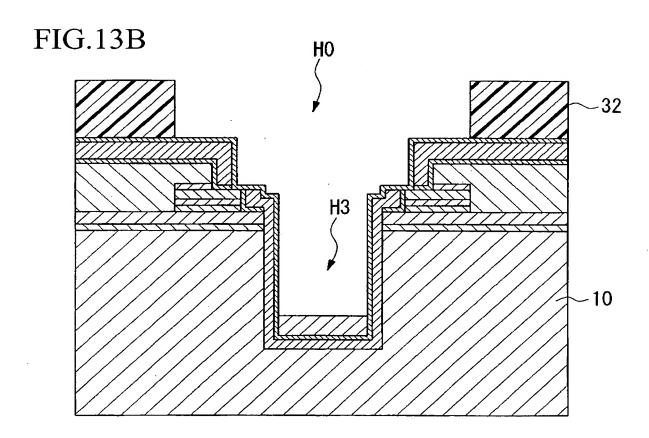




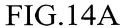
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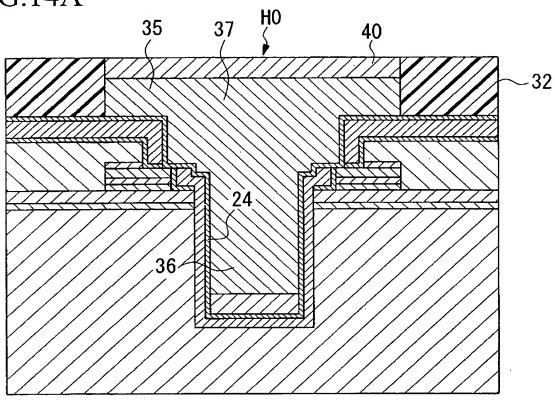
FIG.13A

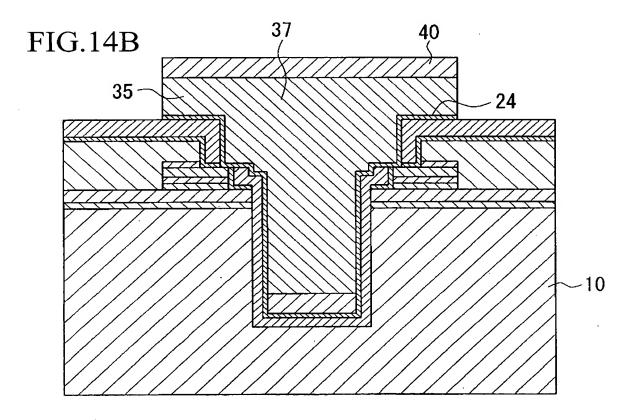




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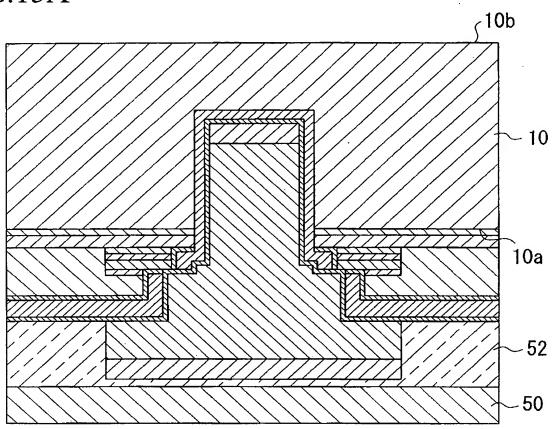


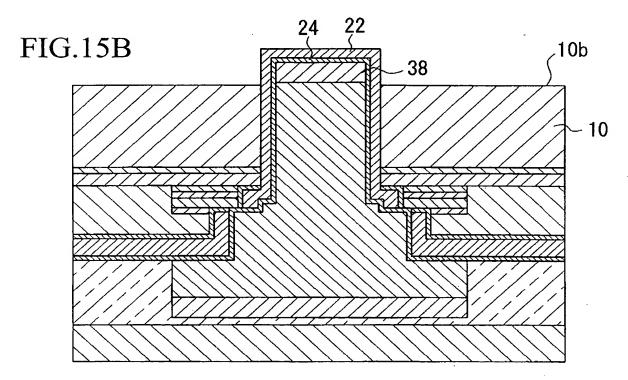




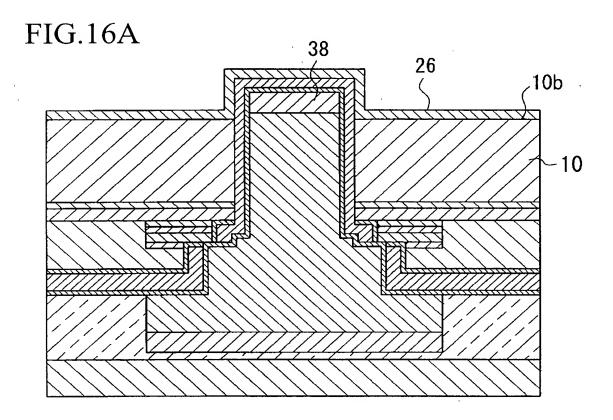
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FIG.15A





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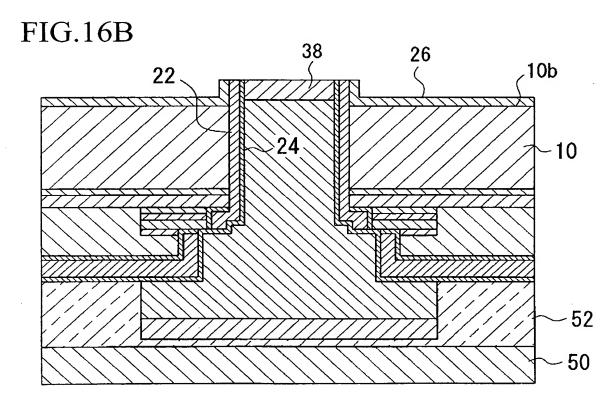
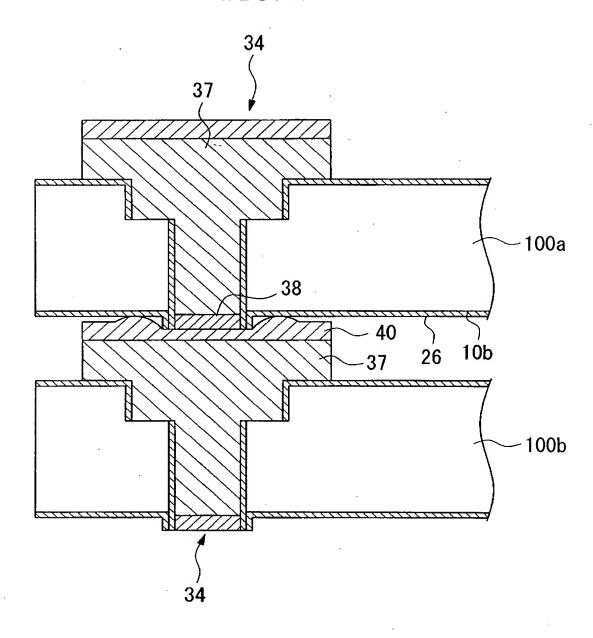
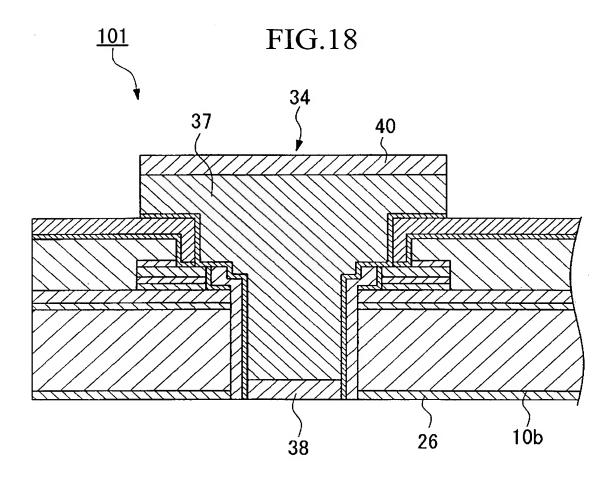
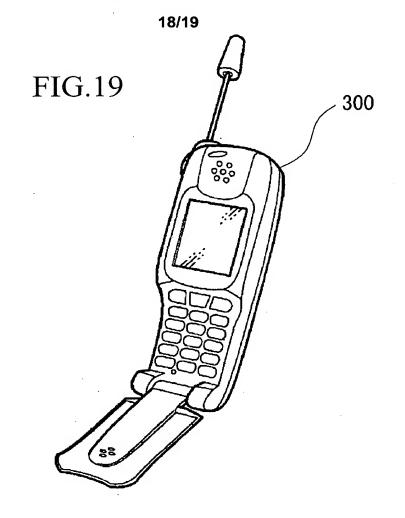


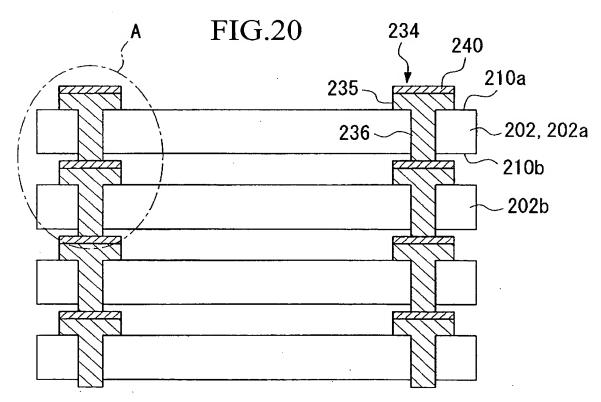
FIG.17





Title: METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE, SEMICONDUCTOR DEVICE, CIRCUIT SUBSTRATE AND ELECTRONIC APPARATUS





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Inventor(s): Ikuya MIYAZAWA Atty. Ref.: 9319G-000739

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FIG.21

